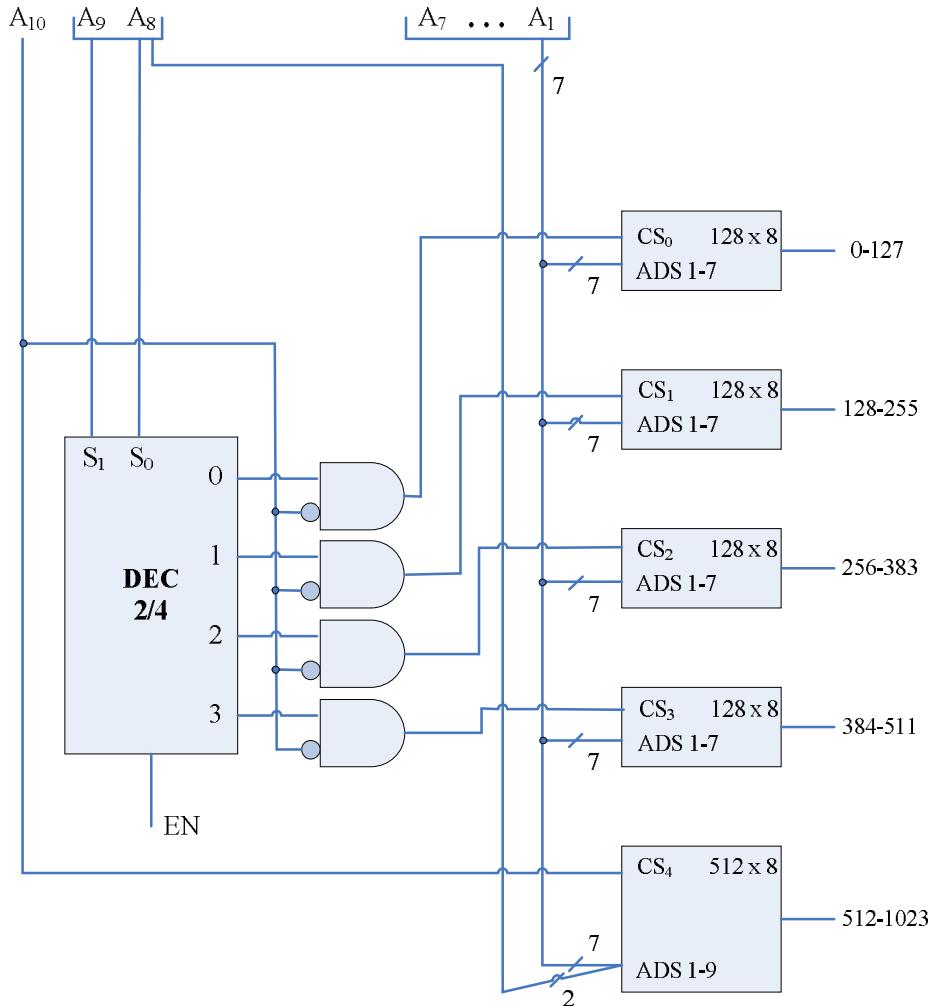


## Vježbe VIII

### Memorije velikog kapaciteta

1. Prikazati organizaciju memorije 1Kx8 koristeći memorijske čipove 128x8 (4 komada) i 512x8 (1 komad), dekoder DEC2/4 i potreban broj logičkih kola.



	A <sub>10</sub>	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	
D <sub>0</sub>	0	0	0	0	0	0	0	0	0	0	0-127
D <sub>0</sub>	0	0	0	1	1	1	1	1	1	1	
D <sub>1</sub>	0	0	1	0	0	0	0	0	0	0	128-255
D <sub>1</sub>	0	0	1	1	1	1	1	1	1	1	
D <sub>2</sub>	0	1	0	0	0	0	0	0	0	0	256-383
D <sub>2</sub>	0	1	0	1	1	1	1	1	1	1	
D <sub>3</sub>	0	1	1	0	0	0	0	0	0	0	384-511
D <sub>3</sub>	0	1	1	1	1	1	1	1	1	1	
	1	0	0	0	0	0	0	0	0	0	512-1023
	1	1	1	1	1	1	1	1	1	1	

$$CS_0 = D_0 \overline{A}_{10}$$

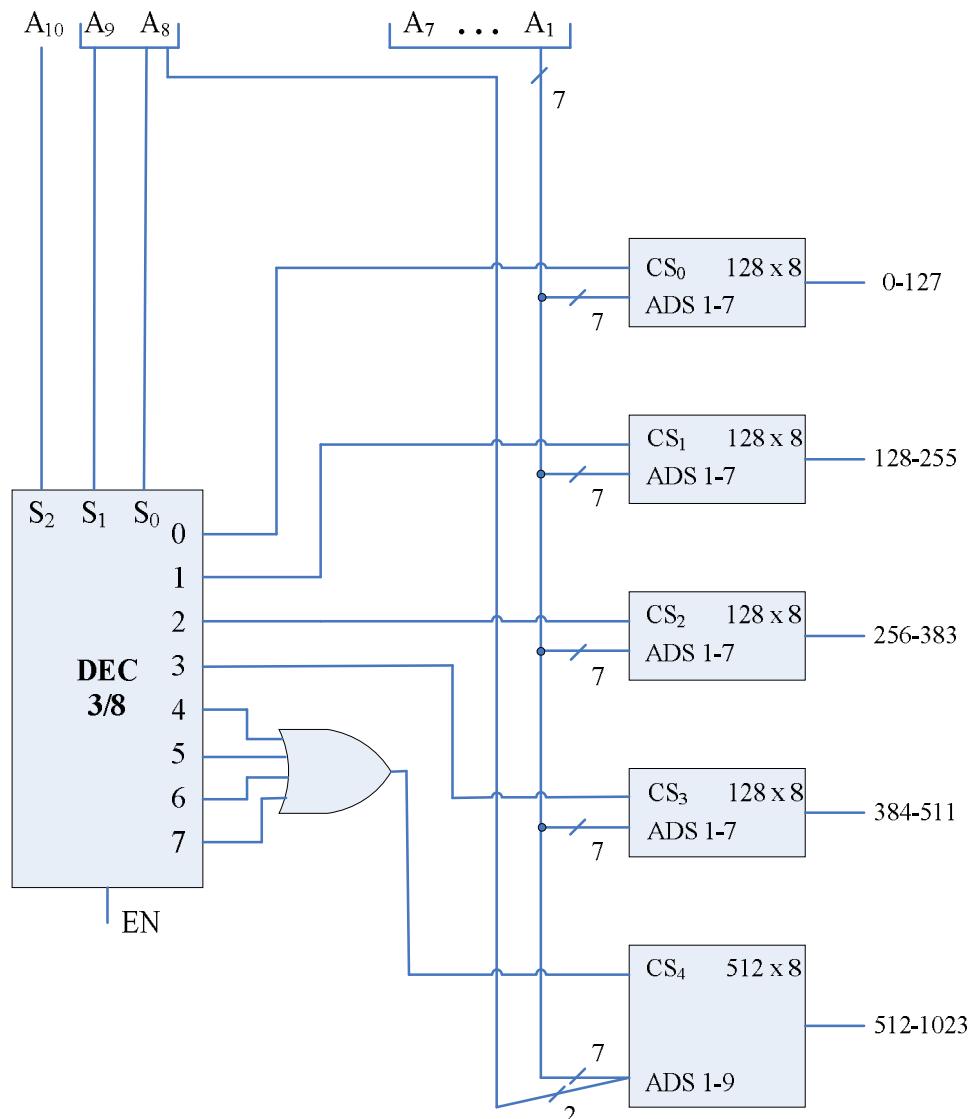
$$CS_1 = D_1 \overline{A}_{10}$$

$$CS_2 = D_2 \overline{A}_{10}$$

$$CS_3 = D_3 \overline{A}_{10}$$

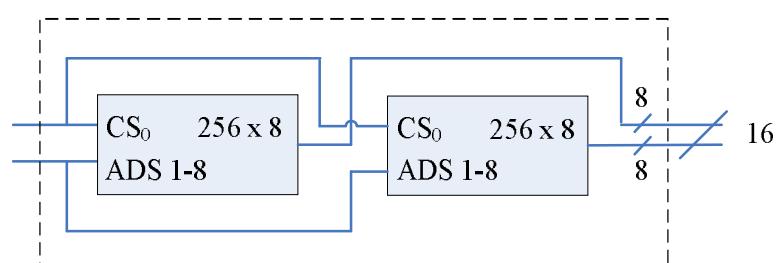
$$CS_4 = A_{10}$$

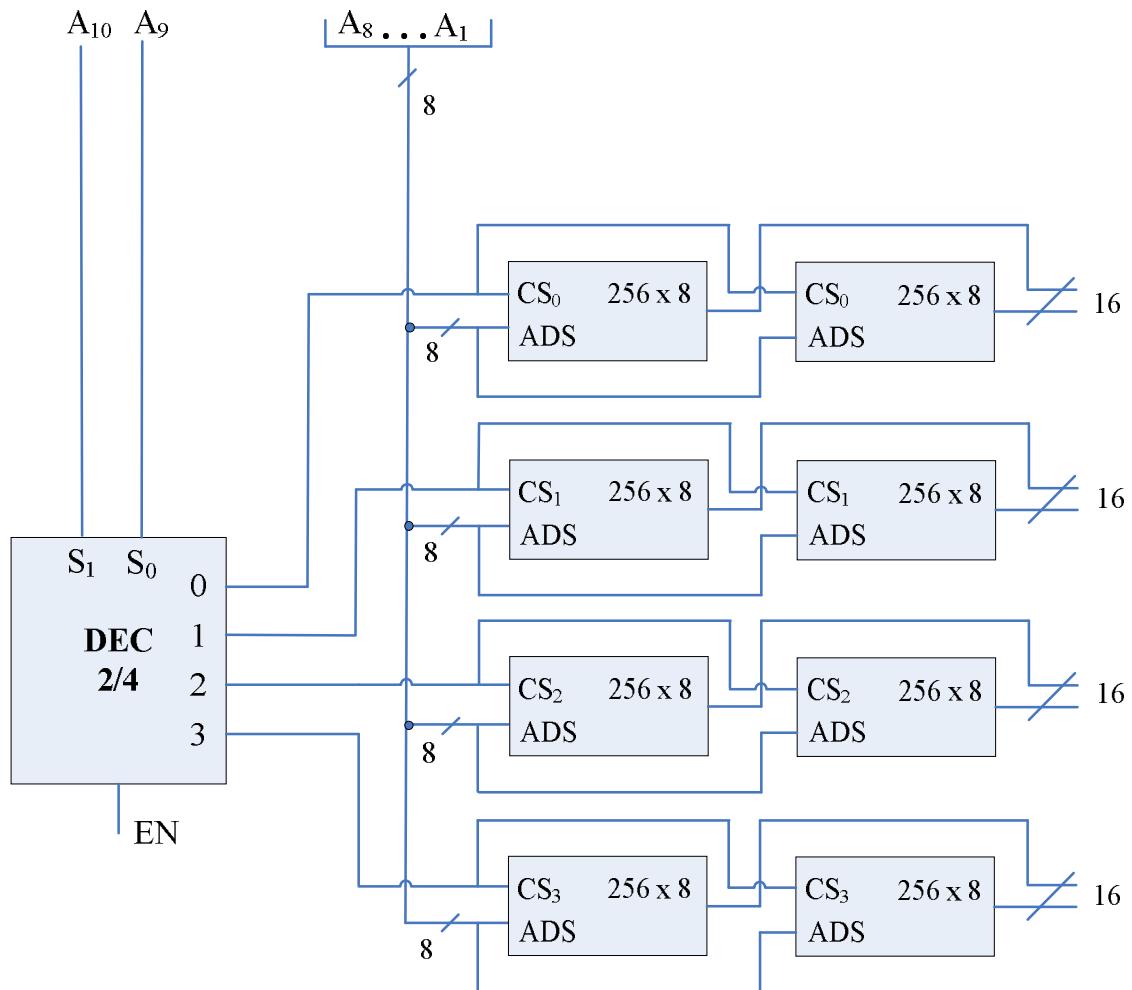
Realizovati prethodnu memoriju koristeći dekoder DEC 3/8. (tabela ostaje ista jer se nijesu mijenjali kapaciteti memorijskih čipova!)



2. Prikazati organizaciju memorije 1Kx16 koristeći memorijске čipove 256x8 (8 komada), dekoder DEC 2/4 i potrebna logička kola.

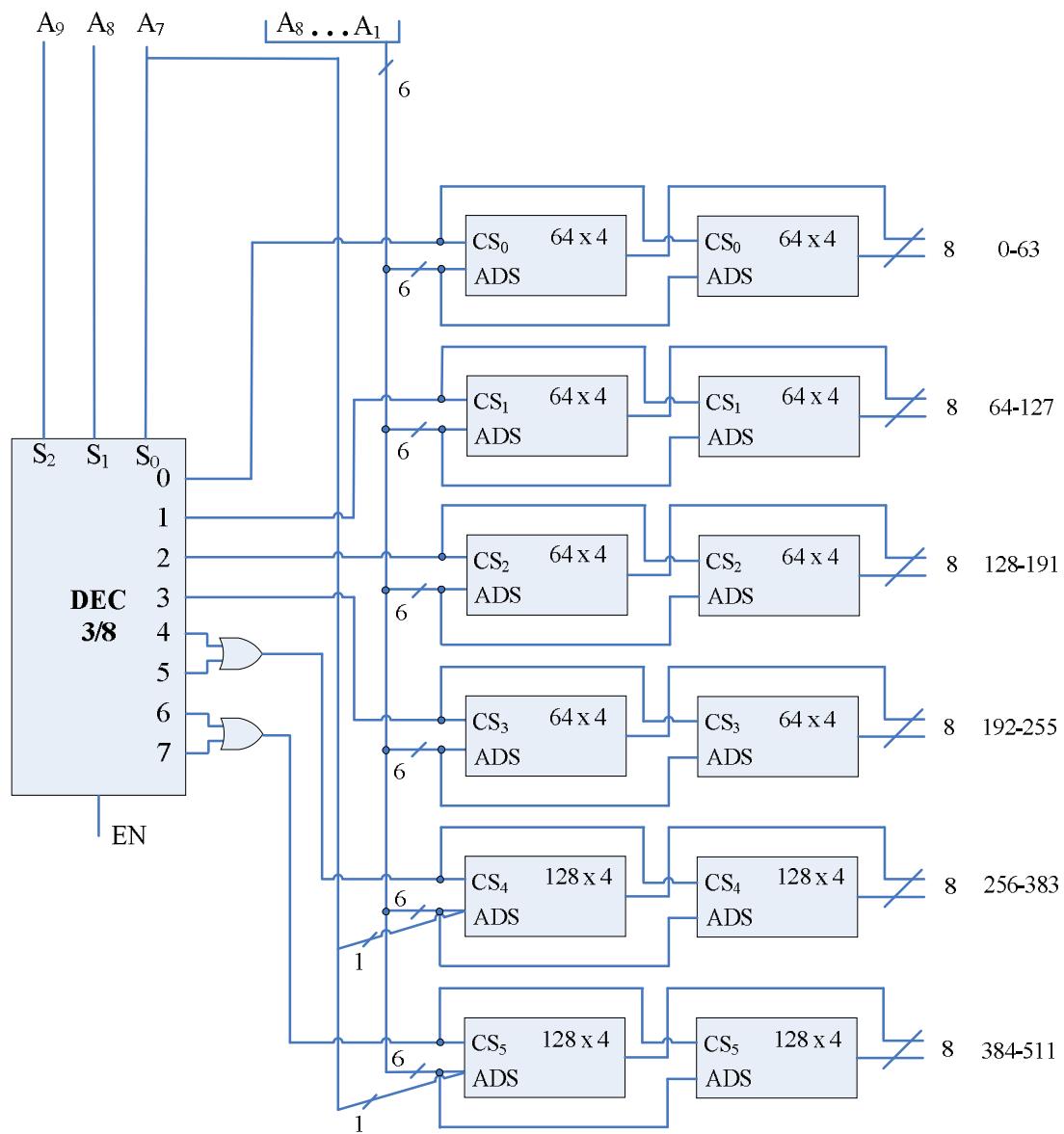
Formiranje memorijski čipova 256x16 od čipova 256x8 (kapacitet čipa se na ovaj način NE povećava!)



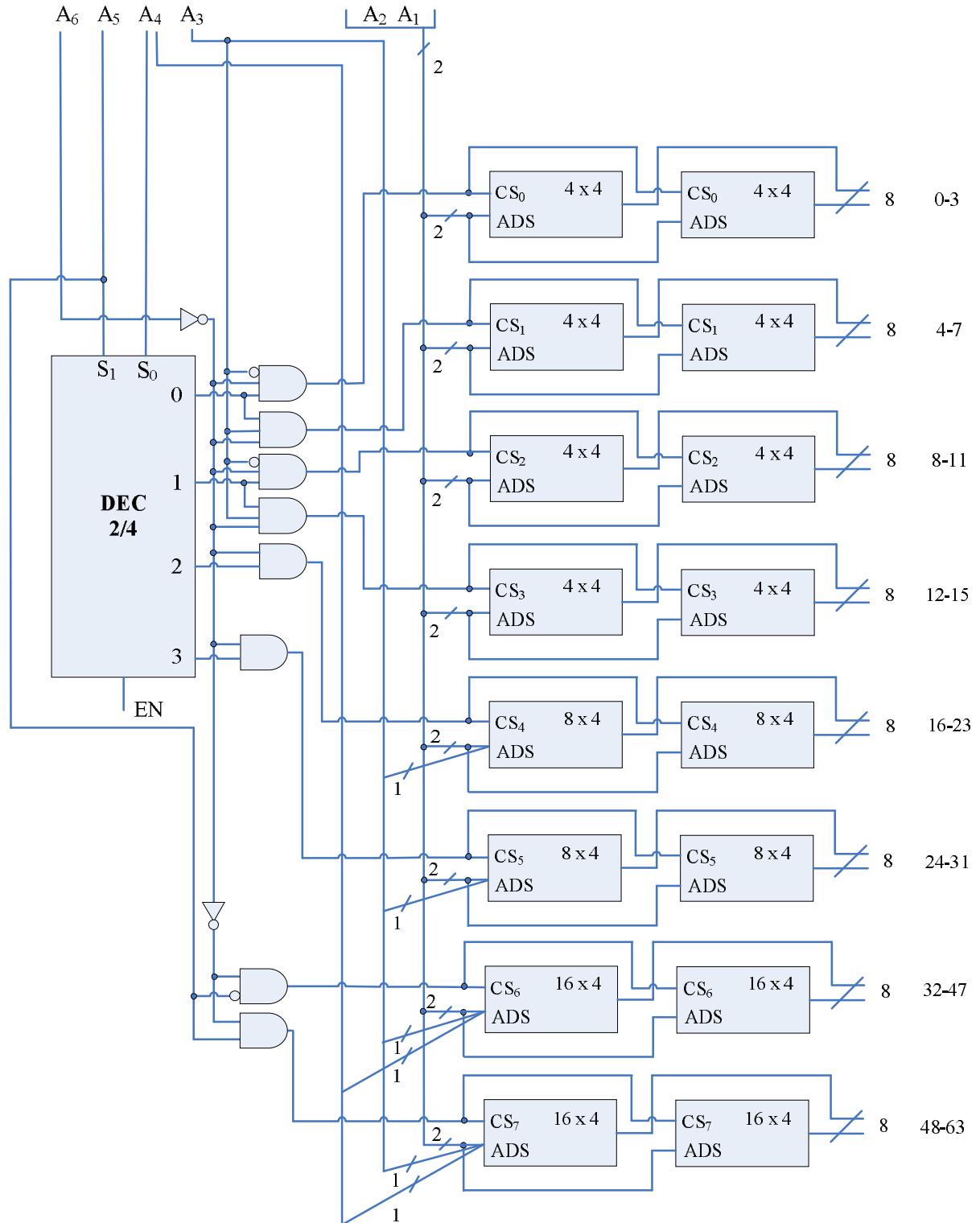


	A <sub>10</sub>	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	
D <sub>0</sub>	0	0	0	0	0	0	0	0	0	0	0-255
D <sub>0</sub>	0	0	1	1	1	1	1	1	1	1	
D <sub>1</sub>	0	1	0	0	0	0	0	0	0	0	256-511
D <sub>1</sub>	0	1	1	1	1	1	1	1	1	1	
D <sub>2</sub>	1	0	0	0	0	0	0	0	0	0	512-767
D <sub>2</sub>	1	0	1	1	1	1	1	1	1	1	
D <sub>3</sub>	1	1	0	0	0	0	0	0	0	0	768-1023
D <sub>3</sub>	1	1	1	1	1	1	1	1	1	1	

3. Prikazati organizaciju memorije 512x8 koristeći memoriske čipove 64x4 (8 komada) i 128x4 (4 komada), dekoder 3/8 i potrebna logička kola.



4. Prikazati organizaciju memorije 64x8 koristeći memorijske čipove 16x4 (4 komada), 8x4 (5 komada) i 4x4 (10 komada), dekoder 2/4 i potrebna logička kola.



Realizacija korišćenjem prve tabele

	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	
D <sub>0</sub>	0	0	0	0	0	0	0-3
D <sub>0</sub>	0	0	0	0	1	1	
D <sub>0</sub>	0	0	0	1	0	0	4-17
D <sub>0</sub>	0	0	0	1	1	1	
D <sub>1</sub>	0	0	1	0	0	0	8-11
D <sub>1</sub>	0	0	1	0	1	1	
D <sub>1</sub>	0	0	1	1	0	0	12-15
D <sub>1</sub>	0	0	1	1	1	1	
D <sub>2</sub>	0	1	0	0	0	0	16-23
D <sub>2</sub>	0	1	0	1	1	1	
D <sub>3</sub>	0	1	1	0	0	0	24-31
D <sub>3</sub>	0	1	1	1	1	1	
	1	0	0	0	0	0	32-47
	1	0	1	1	1	1	
	1	1	0	0	0	0	48-63
	1	1	1	1	1	1	

Moguće je izvršiti i ovakvo grupisanje:

	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	
D <sub>0</sub>	0	0	0	0	0	0	0-3
D <sub>0</sub>	0	0	0	0	1	1	
D <sub>0</sub>	0	0	0	1	0	0	4-17
D <sub>0</sub>	0	0	0	1	1	1	
D <sub>0</sub>	0	0	1	0	0	0	8-11
D <sub>0</sub>	0	0	1	0	1	1	
D <sub>0</sub>	0	0	1	1	0	0	12-15
D <sub>0</sub>	0	0	1	1	1	1	
D <sub>1</sub>	0	1	0	0	0	0	16-23
D <sub>1</sub>	0	1	0	1	1	1	
D <sub>1</sub>	0	1	1	0	0	0	24-31
D <sub>1</sub>	0	1	1	1	1	1	
D <sub>2</sub>	1	0	0	0	0	0	32-47
D <sub>2</sub>	1	0	1	1	1	1	
D <sub>3</sub>	1	1	0	0	0	0	48-63
D <sub>3</sub>	1	1	1	1	1	1	